

	Class	Subclass
ISSUE CLASSIFICATION		

PATENT NUMBER

<p>TTS</p> <p>O.I.P.E.</p> <p>SCANNED <i>8/2</i> Q.A. <i>/10</i></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/719466	CONT/PRIOR D F	CLASS 361	SUBCLASS 2139	ART UNIT 2836	EXAMINER / eia
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Dirk Tillerburg

Protective circuit for electronic modules, especially driver modules

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
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